

## INFORMATION DISCLOSURE STATEMENT

FORM PTO 1449 (modified)

U.S. DEPARTMENT OF COMMERCE  
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LIST OF REFERENCES CITED BY APPLICANT(S)  
(Use several sheets if necessary)

OCT 4 2004

APPLICANT  
Hiroshi HAYASHINO et al.FILING DATE  
October 10, 2001

Date Submitted to PTO: October 4, 2004

GROUP  
2661

Technology Center 2600

2667

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO	
av	AJ	0 939 510	09/99	EP	—	—		
	AK							
	AL							
	AM							
	AN							

## OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

av	AO	IEEE: "IEEE Standard for a High Performance Serial Bus - Link layer specification" IEEE STD 1394-1995, July 1996, XP002287654.
	AP	
	AQ	

EXAMINER

DATE CONSIDERED

8/3/05

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.